CLOCK\_ENABLE\_INPUT\_A=BYPASS

CLOCK\_ENABLE\_OUTPUT\_A=BYPASS

INTENDED\_DEVICE\_FAMILY="Cyclone II"

NUMWORDS\_A=32

OPERATION\_MODE=SINGLE\_PORT

OUTDATA\_ACLR\_A=NONE

OUTDATA\_REG\_A=UNREGISTERED

POWER\_UP\_UNINITIALIZED=FALSE

RAM\_BLOCK\_TYPE=M4K

WIDTHAD\_A=5

WIDTH\_A=8

WIDTH\_BYTEENA\_A=1

DEVICE\_FAMILY="Cyclone II"

address\_a

clock0

q\_a